

This listing of claims replaces all prior versions, and listings, of claims in this application.

**Listing of Claims:**

1. (Currently Amended) A liquid crystal display, comprising:
  - a substrate;
  - a first gate line, comprising a first portion and a second portion;
  - a second gate line, said first gate line and said second gate line being disposed on said substrate and being insulated from each other; said first gate line being non-parallel to said second gate line;
  - an insulation layer disposed above said first gate line and said second gate line;
  - a data line, said data line and said first portion overlapping, said insulation layer being disposed between said data line and said first portion; and
  - a pixel electrode disposed above said data line, said pixel electrode and said second portion overlapping.
2. (Original) The liquid crystal display according to claim 1, wherein said first portion is different from said second portion.
3. (Original) The liquid crystal display according to claim 1, wherein said pixel electrode and said second gate line overlap.
4. (Original) The liquid crystal display according to claim 1, wherein said liquid crystal display further comprises a semiconductor layer disposed above said insulation layer, said semiconductor layer comprises an undoped amorphous silicon layer and a doped amorphous

silicon layer.

5. (Original) The liquid crystal display according to claim 1, wherein said liquid crystal display further comprises a passivation layer disposed above said data line.
6. (Original) The liquid crystal display according to claim 1, wherein said liquid crystal display further comprises a low-dielectric-constant layer disposed above said passivation layer.
7. (Original) The liquid crystal display according to claim 1, wherein said first gate line comprises a pair of gate lines, said pair of gate lines are parallel to each other and separated by a gap.
8. (Original) The liquid crystal display according to claim 7, wherein said data line, said gap and a part of said pair of gate lines overlap.
9. (Currently Amended) A liquid crystal display, comprising:
  - a substrate;
  - a first gate line, comprising a first portion and a second portion;
  - a second gate line, said first gate line and said second gate line being disposed on said substrate and being insulated from each other; said first gate line being non-parallel to said second gate line;
    - an insulation layer disposed above said first gate line and said second gate line;
    - a data line, said data line and said first portion overlapping, said insulation layer being disposed between said data line and said first portion; and
    - a pixel electrode disposed above said data line, said pixel electrode and said second portion overlapping;

wherein said first portion is different from said second portion.

10. (Original) The liquid crystal display according to claim 9, wherein said pixel electrode and said second gate line overlap.
11. (Original) The liquid crystal display according to claim 9, wherein said liquid crystal display further comprises a semiconductor layer disposed above said insulation layer, said semiconductor layer comprises an undoped amorphous silicon layer and a doped amorphous silicon layer.
12. (Original) The liquid crystal display according to claim 9, wherein said liquid crystal display further comprises a passivation layer disposed above said data line.
13. (Original) The liquid crystal display according to claim 9, wherein said liquid crystal display further comprises a low-dielectric-constant layer disposed above said passivation layer.
14. (Original) The liquid crystal display according to claim 9, wherein said first gate line comprises a pair of gate lines, said pair of gate lines are parallel to each other and separated by a gap.
15. (Original) The liquid crystal display according to claim 14, wherein said data line, said gap and a part of said pair of gate lines overlap.
16. (Currently Amended) A manufacture method of a liquid crystal display, said method comprising:
  - providing a substrate;
  - forming a first gate line and a second gate line, said first gate line and said second gate line being disposed on said substrate and being insulated from each other; said first gate

line being non-parallel to said second gate line;  
forming an insulation layer disposed above said first gate line and said second gate line;  
forming a data line, said data line and a first portion of said first gate line overlapping,  
said insulation layer being disposed between said data line and said first portion;  
forming a passivation layer disposed above said data line;  
forming a low-dielectric-constant layer disposed above said passivation layer; and  
forming a pixel electrode disposed above said low-dielectric-constant layer, said pixel  
electrode and a second portion of said first gate line overlapping;  
wherein said first portion is different from said second portion.

17. (Original) The liquid crystal display according to claim 16, wherein said liquid crystal display further comprises a semiconductor layer disposed above said insulation layer, said semiconductor layer comprises an undoped amorphous silicon layer and a doped amorphous silicon layer.